

KIEV, Ukraine, December 2 2010 - Genesys Ltd, memory compilers developer and **POLYTE DA Software Corp**

., a provider of semiconductor design software, signed Collaboration Agreement. The main targets of Agreement are

PowerDRC/LVS

software evolution, rule deck testing, performance testing and benchmarking and also product advertising.

About POLYTEDA Software Corporation:

POLYTEDA Software Corporation is a rapidly growing design automation company, focused on providing fast, accurate and affordable verification solutions for electronic design companies.

For more information you can visit: www.polyteda.com .

